

14P4 Doc's/PCT/PTO 06 JAN 2006

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Dated: January 4, 2005

Signature: *Thomas A. Miller*
(Thomas A. Miller)

Docket No.: 28944/50009
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Francois Costa, et al.

Application No.: 10/555,344

Filed: November 1, 2005

Art Unit: Not Yet Assigned

For: An Electronic Circuit with an Integrated
Piezoelectric Transformer

Examiner: Not Yet Assigned

INFORMATION DISCLOSURE STATEMENT (IDS)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed within three months of the U.S. filing date (37 CFR 1.97(b)(1)).

Copies of the Foreign Patent and Non Patent Literature Documents listed on the PTO/SB/08 Form are enclosed.

In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists. In accordance with 37 CFR 1.97(h), the filing of this Information Disclosure statement shall not be construed to be an

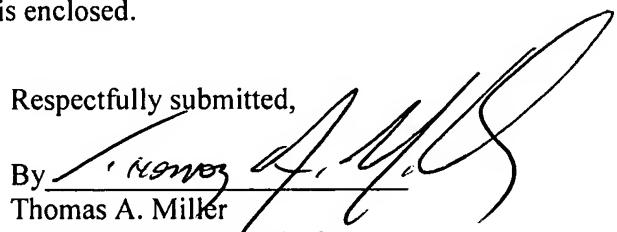
admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

It is submitted that the Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed references.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 50-3629, under Order No. 28944/50009. A duplicate copy of this paper is enclosed.

Dated: January 4, 2005

Respectfully submitted,

By 
Thomas A. Miller

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Substitute for form 1449A/B/PTO				Complete if Known	
				Application Number	10/555,344
				Filing Date	November 1, 2005
				First Named Inventor	Francois Costa, et al.
				Art Unit	Not Yet Assigned
				Examiner Name	Not Yet Assigned
Sheet	1	of	2	Attorney Docket Number	28944/50009

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)			
		EP 0 689 254-A1	12-27-1995	Motorola, Inc.	T ⁶

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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
		French Preliminary Search Report FR 0305601; report dated January 29, 2004.			
		International Search Report PCT/FR2004/001040; report dated November 4, 2004.			
		Written Opinion of the International Search Administration dated January, 2004.			
		"A New MOFSET & IGBT Gate Drive Insulated by a Piezoelectric Transformer", Vasic D et al., 2001 IEEE 32 nd Annual Power Electronics Specialists Conference, Vancouver, Canada, 17-21 June 2001, pages 1479-1484.			
		"Commande de Transistor a Grille Isolee Par Transformateurs Piezoelectriques: Bras Complet", Vasic and al, EPF 2002, Montpellier, November 2002.			
		"Piezoelectric Transformer Operating in Thickness Extensional Vibration and its Application to Switching Converter", Zaitsu and al, PESC 94.			
		"Coreless printed circuit board (PCB) Transformer for Power MOFSET & IGBT gate drive circuit", HUI and al. IEEE, Trans. Power Electron., Vol. 14, pages 431-437, May 1999.			
		"Some Electromagnetic Aspects of Coreless PCB Transformers", HUI and al. IEEE, Trans. Power Electron., Vol. 14, pages 805-810, July 2000.			
		"Optimal Operation of coreless PCB Transformer Isolated Gate Drive Circuits with Wide Switching Frequency Range", HUI and al., IEEE transactions on Power Electronics, Vol. 14, pages 506-514, May 1999.			
		"Piezoelectric Transformer for Complementary MOFSET and IGBT Gate Drive Circuit", Vasic and al. EPE 2003, Toulouse, September 2003.			
		"New Design Method of Piezoelectric Transformer Considering High-Power Characteristics of Various Compositions Ceramics", Hirose and al. 1998, pages 953-959.			
		"Design of Piezo Transformers and Assemblies", Yerofeyev, Electrical Tehnology, N°4, 1996, pages 155-164.			

Examiner Signature	Date Considered
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Substitute for form 1449A/B/PTO				<i>Complete if Known</i>	
				Application Number	10/555,344
				Filing Date	November 1, 2005
				First Named Inventor	Francois Costa, et al.
				Art Unit	Not Yet Assigned
				Examiner Name	Not Yet Assigned
Sheet	2	of	2	Attorney Docket Number	28944/50009

	"Design Considerations of Multi-Layer Piezoelectric Transformer", Prieto and al., 16 th Annual Applied Power Electronics Conference, Apec 2001, Anaheim, USA 4-8 March 2001, pages 1258-1263.	
	"A new Method to Design Piezoelectric Transformer used in MOSFET and IGBT Gate Drive Circuits", Vasic and al., 2003, IEEE 34 th Annual Power Electronics Specialist Conference, PESC 2003, Acapulco, Mexico, 15-19 June 2003, pages 307-312.	
	"Piezoelectric Technology Primer", James R. Phillips, Linear Technology, AN81-12 – AN81-21.	

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Application No.: 10/555,344

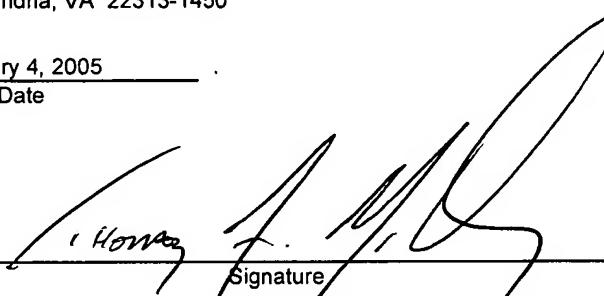
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IDS (Citation) by Applicant (15 References) (2 pages)
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